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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)

Application Number	10803663
Filing Date	2004-03-18
First Named Inventor	DeWitt, Jr. et al.
Art Unit	2192
Examiner Name	Dan, Tuan Quang Ben C. Wang
Attorney Docket Number	AUS920030548US1

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Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
BW	1	6105129		2000-08-15	Meier et al.	
BW	2	6928521	B1	2005-08-09	Burton et al.	
BW	3	6598153	B1	2003-07-22	Flachs et al.	
BW	4	5875294		1999-02-23	Roth et al.	
BW	5	6073109		2000-06-06	Flores et al.	
BW	6	6871298	B1	2005-03-22	Cavanaugh et al.	
BW	7	5555432		1996-09-10	Hinton et al.	
BW	8	6145077		2000-11-07	Sidwell et al.	

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Application Number		10803663
Filing Date		2004-03-18
First Named Inventor	DeWitt, Jr. et al.	
Art Unit	2192	
Examiner Name	Dam, Tuan Quang Ben C. Wang	
Attorney Docket Number	AUS920030548US1	

BW	9	6865666	B2	2005-03-08	Yoshida et al.	
BW	10	5590352		1996-12-31	Zuraski et al.	
BW	11	6199204	B1	2001-03-06	Donohue	
BW	12	6202207	B1	2001-03-13	Donohue	

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BW	1	20050257092	A1	2005-11-17	Alexander et al.	
BW	2	20050177822		2005-08-22	Kuch et al.	
BW	3	20030041096	A1	2003-02-27	Johnson	

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BW	2	5822790		1998-10-13	Mehrotra	
BW	3	6848029	B2	2005-01-25	Coldewey	
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BW	5	6782454	B1	2004-08-24	Damron	
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BW	7	6687794	B2	2004-02-03	Malik	
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BW	9	6973542	B1	2005-12-06	Schmuck et al.	
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BW	1	"Method for the dynamic prediction of nonsequential memory accesses", September 25, 2002, pp. 1-4, ip.com IPCOM000009888D	<input type="checkbox"/>
BW	2	"Cache Miss Director - A Means of Prefetching Cache Missed Lines", August 1, 1982, IBM Technical Disclosure Bulletin, Vol. 25, Issue 3A, pp.1286	<input type="checkbox"/>

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BW	2	7035996	B2	2006-04-25	Woodall et al.	

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BW	1	TANENBAUM, "Structured Computer Organization", Prentice Hall, 1984, pp. 10-12.	<input type="checkbox"/>

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STATEMENT BY APPLICANT

Application Number 10/803,663
 Filing Date 03-18-2004
 First Named Inventor DeWitt, Jr. et al.
 Examiner Name Unknown Ben C. Wang
 Art Unit 2122 2192
 Attorney Docket No. AUS920030548US1

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
BW AA	6,149,318	11-21-2000	Chase et al.	
BW AB	6,185,671 B1	02-06-2001	Pentovski et al.	
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Examiner Name	Unknown Ben C. Wang
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BW AA	5805879	09-08-1998	Hervin et al.	
BW AB	6647301 B2	11-11-2003	Sederlund et al.	
BW AC	6735666 B1	05-11-2004	Koning	
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APPLICANT DeWitt, Jr. et al.			FILING DATE 03/18/2004		GROUP ART UNIT Not Assigned 2192	
U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/SUBCLASS	FILING DATE	
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BW	AB 6,330,662 B1	Dec. 11, 2001	Patel et al.	712/236	Feb. 23, 1999	
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DATE CONSIDERED 04/17/2007			EXAMINER /Ben Wang/			
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BW	BB	2002/0129309	Sep. 12, 2002	Floyd et al.	714/724	Dec. 18, 2000
BW	BC	2001/0032305	Oct. 18, 2001	Barry	712/34	Feb. 23, 2001
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EXAMINER INITIAL	DOCUMENT NO.		PUBLICATION DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO	
BW	BH	JP2000029731	Dec. 8, 1999	Japan	G06F 9/38		X
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BW	BO	IBM Research Disclosure Bulletin 444188, "Enable Debuggers as an Objective Performance Measurement Tool for Software Development Cost Reduction", April 2001, pp. 686-688.

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EXAMINER INITIAL	APPLICATION NO./ ATTY. DOCKET NO.	APPLICANT	TITLE	FILING DATE
BW	BP 09/435,069 AT9-99-491	Davidson et al.	Method and Apparatus for Instruction Sampling for Performance Monitoring and Debug	Nov. 4, 1999
BW	BQ 08/538,071 AUT919950055US1	Gover et al.	Method and System for Selecting and Distinguishing an Event Sequence using an Effective Address in a Processing System	Oct. 2, 1995

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BW	BR	Not Assigned AUS920030477US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction Execution and Data Accesses	Sep. 30, 2003
BW	BS	Not Assigned AUS920030478US1	Levine et al.	Method and Apparatus for Selectively Counting Instructions and Data Accesses	Sep. 30, 2003
BW	BT	Not Assigned AUS920030479US1	DeWitt, Jr. et al.	Method and Apparatus for Generating Interrupts Upon Execution of Marked Instructions and Upon Access to Marked Memory Locations	Sep. 30, 2003
BW	BU	Not Assigned AUS920030480US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Data Accesses and Instruction Executions that Exceed a Threshold	Sep. 30, 2003
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BW	BX	Not Assigned AUS920030483US1	Levine et al.	Method and Apparatus to Autonomically Select Instructions for Selective Counting	Sep. 30, 2003
BW	BY	Not Assigned AUS920030484US1	Levine et al.	Method and Apparatus to Autonomically Count Instruction Execution for Applications	Sep. 30, 2003
BW	BZ	Not Assigned AUS920030485US1	Levine et al.	Method and Apparatus to Autonomically Take an Exception on Specified Instructions	Sep. 30, 2003
BW	CA	Not Assigned AUS920030486US1	Levine et al.	Method and Apparatus to Autonomically Profile Applications	Sep. 30, 2003
BW	CB	Not Assigned AUS920030487US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction and Memory Location Ranges	Sep. 30, 2003

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Art Unit	2122 2192	
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